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EFS ID:	7223773			
Application Number:	10554634			
International Application Number:				
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Title of Invention:	LOW-VOLTAGE IC-CIRCUIT			
First Named Inventor/Applicant Name:	Hubert Kaeslin			
Customer Number:	02292			
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1 Issue Fee Payment (PTO-85B)	20100316lF.pdf	104132	no	1	
		2e9a86b77b649d2fb3e39e70E114d31505a ab19			

Warnings:

Informations

Information:					
2	Fee Worksheet (PTO-875)	fee-info.pdf	31767 Te/A4bbfffee::dcef3145du6cd7e70928890 3523	no	2

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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